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 BP Core, **DMA** Controllers, RT Clock, W Timer, SP ... - analog.com

 ... The memory **DMA** controller provides high bandwidth data ... ory mapped registers (MMRs) at addresses **near** the top of ... The ADSP-BF539/ADSP-BF539F **processor** contains a ...

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HP Hofstee - Proceedings of the 11th International Symposium on High- ..., 2005 - doi.ieeecomputersociety.org

 ... as well as a memory mapped **mailbox** per SPE ... The SPE **processor** incorporates many of the characteristics that ... for it to achieve high frequency and **near** optimal CPI ...

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A Kumar, G Senthilkumar, M Krishna, N Jayam, PK ... - Proceedings of the 7th International Conference on ... - Springer

 ... 1 A **mailbox** message is a fast communication mechanism for ... OS support that is expected in the **near** future ... significant obstacle to the use of the Cell **processor**. ...

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HF Silverman, WR Patterson, JL Flanagan, D ... - Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., ..., 1997 - ieeexplore.ieee.org

 ... boxes, called microphone mod- ules, mounted **near** the array ... The DSP **processor** programs have no in- fluence on ... is used as a **mailbox** for data entering or leaving ...

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HA RAPHAEL - Microprocessors: New Directions for Designers, Selected from ..., 1975 - Hayden Book Co Inc., US

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P Li, Y Lu, S Li, H Wei - International Journal of Computers, Communications &amp; Control, 2006 - journal.univagora.ro

 ... DSP driver would process this **Mailbox** interrupt on the ... multimedia applications in the **near** future ... Instruments, OMAP5910 Dual-Core **Processor** Technical Reference ...

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 ... Outgoing **Mailbox** ... transfer speed is saturated at 127 MB/s, **near** the theoretical ... in co-operation with the readout software running on the front- end **processor**. ...

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 ... Define backdoor access to **mailbox** Define ports for functions and variables ... g on the target **processor**. ... This is very **near** the maximum possible speed for emulation ...

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JE Brewer, LG Miller, IH Gilbert, JF Melia, D ... - Wafer Scale Integration, 1994. Proceedings., Sixth Annual ..., 1994 - [ieeexplore.ieee.org](#)

... In the **near** future the device will ... to/from the **processor** through any of eight **mailbox** registers. ... read or write any location in the **processor's** internal memory. ...

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J Pisano, R Amestica, J Perez - [accelconf.web.cern.ch](#)

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